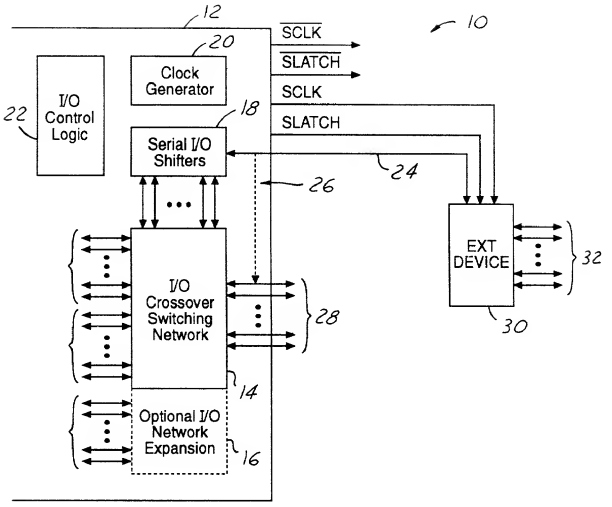


FIG. 1

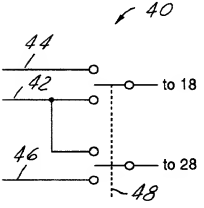


FIG. 2

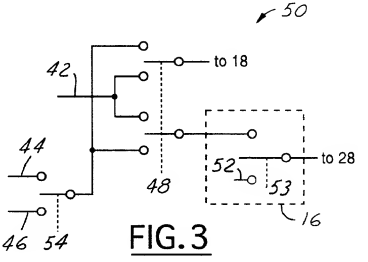


FIG. 3

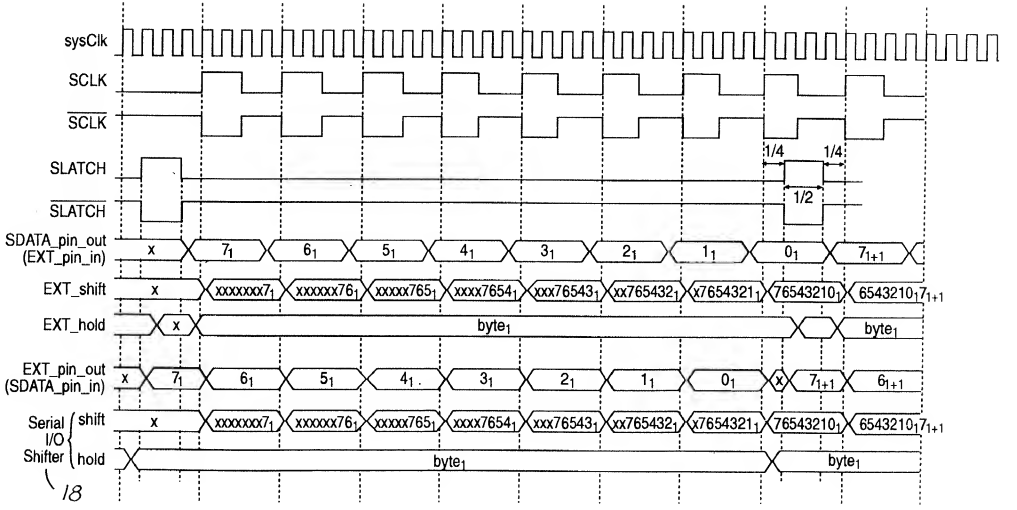
sysClk
SCLK
$\overline{SCLK}$
SLATCH
$\overline{SLATCH}$
1/4
1/4
1/2
SDATA_pin_out (EXT_pin_in)
EXT_shift
EXT_hold
EXT_pin_out (SDATA_pin_in)
Serial I/O Shifter
shift
hold
18

FIG. 5

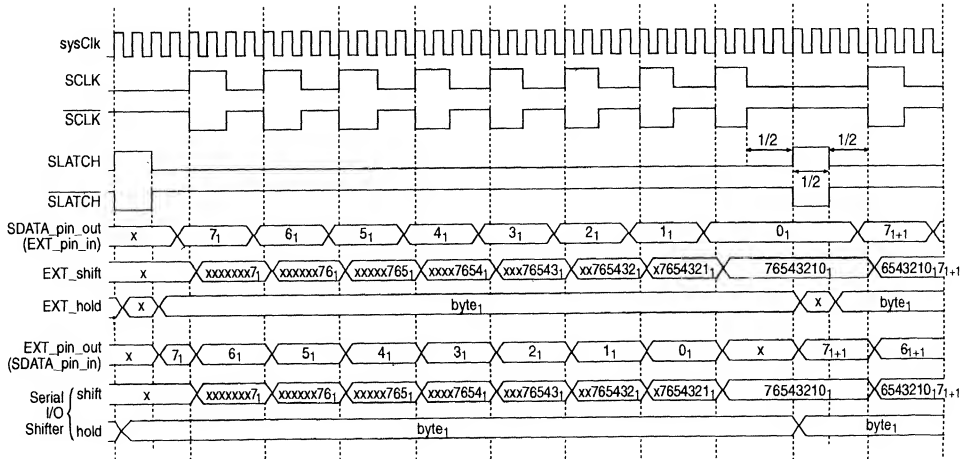
sysClk
SCLK
SCLK
SLATCH
SLATCH
SDATA_pin_out (EXT_pin_in)
EXT_shift
EXT_hold
EXT_pin_out (SDATA_pin_in)
Serial I/O Shifter
shift
hold
1/2
1/2
1/2
x
$7_1$
$6_1$
$5_1$
$4_1$
$3_1$
$2_1$
$1_1$
$0_1$
$7_{1+1}$
xxxxxxx$7_1$
xxxxxx$76_1$
xxxxx$765_1$
xxxx$7654_1$
xxx$76543_1$
xx$765432_1$
x$7654321_1$
$76543210_1$
$6543210_1 7_{1+1}$
$byte_1$
$x$
$byte_1$
$x$
$7_1$
$6_1$
$5_1$
$4_1$
$3_1$
$2_1$
$1_1$
$0_1$
$x$
$7_{1+1}$
$6_{1+1}$
x
xxxxxxx$7_1$
xxxxxx$76_1$
xxxxx$765_1$
xxxx$7654_1$
xxx$76543_1$
xx$765432_1$
x$7654321_1$
$76543210_1$
$6543210_1 7_{1+1}$
$byte_1$
$byte_1$

FIG. 6

Operational Overview - cont'd

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Alternate High Speed I/O Serialization / Deserialization

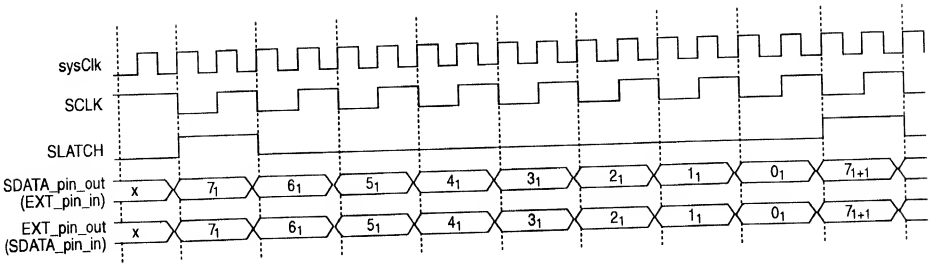


FIG. 7